

PATENT APPLICATION

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application of	:	Martin Wagner et al.
	:	
For	:	METHOD AND BASE CHIP FOR
	:	MONITORING THE OPERATION
	:	OF A MICROCONTROLLER UNIT
	:	
Serial No.	:	10/517,109
	:	
Filed	:	December 7, 2004
	:	
Art Unit	:	2114
	:	
Examiner	:	Joseph O. Schell
	:	
Att. Docket	:	DE 020140
	:	
Confirmation No.	:	1318

DECLARATION UNDER 37 C.F.R. §1.131

I, Martin Wagner, a named inventor, hereby declare and state:

1. This Declaration is submitted as evidence that the subject matter claimed in Claims 1-9 and 11-16 of the above-identified application was invented by the named inventor prior to May 30, 2002, the effective U.S. filing date of U.S. Patent Application Publication No. 2003/0226059.

2. I am the named inventor in the above-identified application.

3. I am an author of the attached invention submission document "Systemchip mit Fehlerstatistik Unterstützung," created prior to May 30, 2002, a true copy of which appears as Exhibit A attached to this Declaration.

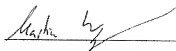
4. In the copies attached hereto, dates and other material that could indicate dates may have been masked out as permitted under the U.S. patent rules.

5. Exhibit A describes a system chip supporting failure statistics and evidences conception of the subject matter recited in claims 1-9 and 11-16 of the above-identified application.

6. Exhibit A describes an invention conceived prior to May 30, 2002, coupled with diligence leading up to a subsequent reduction to practice. This invention is claimed in the above-identified application.

7. I hereby declare that all statements made herein of my own knowledge are true, and that all statements made on information and belief are believed to be true; and further that these statements were made with the knowledge that willful false statements and the like so made are punishable by fine and/or imprisonment under Section 1001 of Title 18 of the United States Code, and that such willful false statements may jeopardize the validity of the application or any patent issuing therefrom.

Date: February 4, 2009

Signature: 

Printed Name: Martin Wagner

ID ABSTRACT			
ID-number: 202107	ent reference: unknown	Patent Engineer: von Laue	To be filled in by GS&S:
Inventors: (name)	(e-mail address)	IS C:	PS: 12/03
Wagner, Martin	martin.wagner@philips.com	L&A	PS:
Muth, Mathias	mathias.muth@philips.com	L&A	PS:
			PPManager:
Short title: SYSTEM CHIP SUPPORTING FAILURE STATISTICS			
OS-codes: 12AUBS		ID Date: 10/04/2002	
Project code:	Action code: 1	Plan Date: 30/06/2002	Action Code Date: 10/04/2002
ABSTRACT			
<p>A system based chip (SBC) is shown, detecting and tracing cyclic failure situations within ECU's in order to prevent a system from permanent high current consumption. Some continuously supplied general-purpose bits within the SBC allow to store failure events using application software and to keep this status information available, even if the application controller was un-powered due to failure or certain low-power mode. A dedicated register within the SBC allows distinguishing between different failure events and thus tracking of different cyclic problems. If a user-defined limit is exceeded, the application can now decide, not starting again but enter immediately a low-power condition.</p> <p>Advantages / Improvements:</p> <p>Application / Use:</p> <p>PE Remarks:</p>			
To be filled in by PE in case of AC 2			
Information is missing:	<input type="checkbox"/> Technical <input type="checkbox"/> Commercial		
Information will be provided by:	name of the person / priority setting meeting time limit <input type="checkbox"/> Inventor(s) <input type="checkbox"/> Priority setting meeting <input type="checkbox"/> Other person		
To be filled in by PE in case of AC 1			
Patent application to be outsourced:	<input type="checkbox"/> Yes <input type="checkbox"/> No		
If, Yes, preferred outside attorney:			
(Please return to local GS&S)			

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#### 4. HAUPT-CLAIMS

- 1) Systemchip, der mindestens einige frei für die Applikation verfügbare Speicherbits zur Verfügung stellt, die permanent von der Batterie versorgt sind und vom Applikations- Microcontroller gelesen und beschrieben werden können.
- 2) Systemchip, der es erlaubt, verschiedene Rücksetzereignisse zu unterscheiden und für den Applikations- Microcontroller zugänglich zu machen.
- 3) Systemchip, der den Schreibzugang zu den frei programmierbaren Speicherbits nur während des Systemstarts zulässt, um fehlerhafte Schreibzugriffe im Betrieb zu verhindern.
- 4) Systemchip, der den Lesezugang zu den frei programmierbaren Speicherbits immer ermöglicht.

#### 5. ABSTRAT

A system basis chip (SBC) is shown, detecting and tracing cyclic failure situations within ECU's in order to prevent a system from permanent high current consumption. Some continuously supplied general-purpose bits within the SBC allow to store failure events using application software and to keep this statistic information available, even if the application controller was un-powered due to failure or certain low-power mode. A dedicated register within the SBC allows distinguishing between different failure events and thus tracking of different cyclic problems. If a user-defined limit is exceeded, the application can now decide, not starting again but enter immediately a low-power condition.